

**In the Specification:**

Please substitute the following paragraph for the paragraph originally beginning at page 10, line 10:

Then, if the high level pulse duration of  $\Phi_{HVEN}$  is terminated at the time  $t_p$ , a high voltage control signal HVP outputted from the circuit of FIG. 4 is activated to a high level and the pumping voltage is charged to the high voltage VPP for program operation. The high voltage VPP may be applied to the source line SL of FIG. 3, for example, which is in signal communication with the bit lines BL1 through BLk.